

Amendment to the Specification

Please re-write the second paragraph of page 8 as follows:

While Figure 2 illustrates one exemplary I/O cell that substantially minimizes voltage reflections by employing DST, the invention is not intended to be limited to the features shown herein. Rather, other implementations that apply such dynamic termination impedance to a received signal may also fall within the scope of the invention. Figure 3 is a schematic diagram of I/O cell 300 in another aspect hereof. I/O cell 300 combines circuitry for duty cycle control (e.g., circuitry 302) with circuitry for dynamic switchable impedance (e.g., circuitry 303). As such, circuitry 302 may operate according to duty cycle controller 102 of Figure 1, and circuitry 303 may operate according to DST 203 of Figure 2.